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CERT. No.: 282Q19070712006



CERT. No.: 282E19070712007

Product Specification

Model: TTC036XRS-01

3.6" TFT Display Module (544*506)

This module uses RoHS material

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Table of Contents

| | |
|--|----|
| Revision History | 3 |
| 1 General Specifications | 4 |
| 2 Pin Assignment | 5 |
| 3 Absolute Maximum Ratings | 6 |
| 4 Electrical Characteristics | 6 |
| 5 INTERFACE TIMING | 8 |
| 6 Optical Characteristics | 10 |
| 7 Environmental / Reliability Test | 13 |
| 8 Mechanical Drawing | 14 |
| 9 Precautions For Use of LCD Modules | 15 |

Revision History

| Rev | Issued Date | Description | Page | Editor |
|-----|-------------|---------------|------|--------|
| 1.0 | Dec.26,2023 | First release | All | |
| | | | | |

1 General Specifications

| Feature | | Specifications |
|----------------------------|------------------------------------|------------------------|
| Display Spec. | LCD type | 3.6 inch |
| | Resolution (H*V) | 544*(RGB)*506 |
| | Technology Type | a-Si TFT |
| | Pixel Configuration | R.G.B. Vertical Stripe |
| | Normally White | Normally Black |
| | Viewing Direction | ALL |
| Mechanical Characteristics | OutlineDimensions (W x H x T) (mm) | 95.46*91.81*2.3 |
| | Active Area(mm) | 83.49*89.76 |
| | With /Without Touch screen | Without |
| | Match Connector Type | OK-14GM040-04 |
| | Backlight Type | LED |
| | Weight (g) | TBD |
| Electrical Characteristics | Interface | 24RGB |
| | Number of color | 16.7M |
| | Driver IC | ST7266-G6-E |

2 Pin Assignment

| NO. | PIN NAME | Description |
|-------|-----------|---|
| 1 | A | Backlight LED Power |
| 2-3 | K | Backlight LED Ground |
| 4 | GND | Ground |
| 5 | VCI-3.3V | Power Supply(3.3V) |
| 6 | RESET | Reset Signal ,Active Low |
| 7 | DISP | display |
| 8 | NC | NC |
| 9 | SDA/NC | Serial input/output signal in serial interface mode/NC |
| 10 | SCK/NC | In Serial Interface, this is used as SCL/.NC |
| 11 | CS/NC | Chip Selection Pin/NC |
| 12 | PCLK | RGB dot clock signal |
| 13 | DE | Data input enable. Display access is enabled when DE is "H" |
| 14 | VSYNC | RGB frame synchronizing signal |
| 15 | HSYNC | RGB line synchronizing signal |
| 16-23 | DB0-DB7 | B0-B7 RGB data input |
| 24-31 | DB8-DB15 | G0-G7 RGB data input |
| 32-39 | DB16-DB23 | R0-R7 RGB data input |
| 40 | GND | Ground |

3 Absolute Maximum Ratings

GND=0V, Ta= 25℃

| Item | Symbol | Value | Unit |
|--------------------------------|-----------------|----------------------|------|
| Power supply voltage for logic | V _{DD} | 3.0~3.6 | V |
| Input voltage | V _{in} | V _{DD} +0.3 | V |
| Operating temperature | Topr | -20 to 70 | ℃ |
| Storage temperature | Tstg | -30 to 80 | ℃ |

Note: Note1: Absolute maximum rating is the limit value beyond which the IC maybe broken.

They do not assure operations.

Note2: Background color changes slightly depending on ambient temperature. This Phenomenon is reversible.

4 Electrical Characteristics

4.1 Driving TFT LCD Panel

| Item | Symbol | Min | Type | Max | Unit | Test condition |
|--------------------------|--------|--------|------|--------|------|----------------|
| Power voltage | VDD | 3.0 | 3.3 | 3.6 | V | - |
| Input logic high voltage | VIH | 0.7VDD | - | VDD | V | |
| Input logic low voltage | VIL | 0 | - | 0.3VDD | V | |

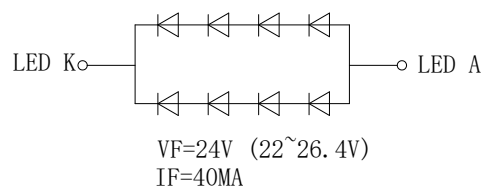
4.2 Driving Backlight

Ta=25℃

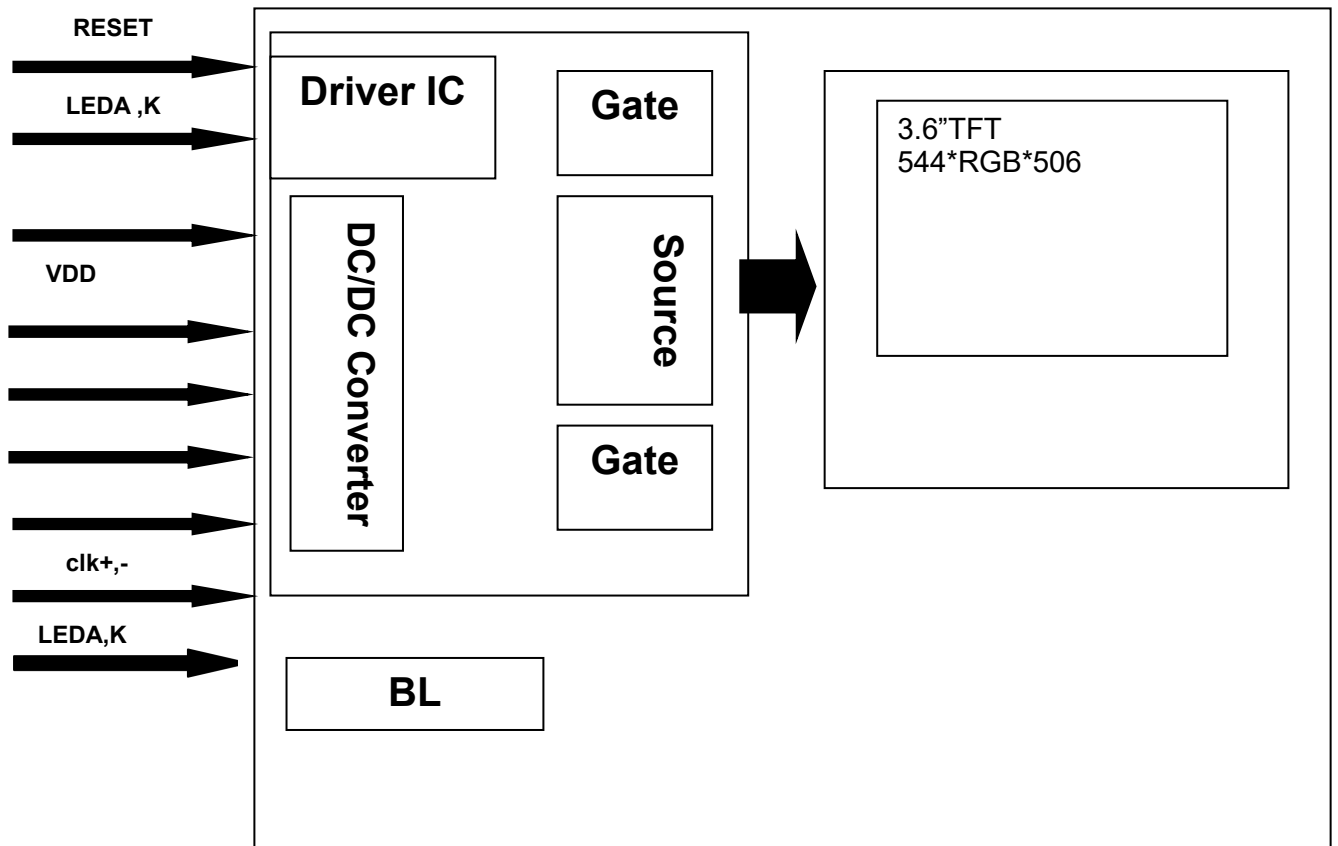
| Item | Symbol | Min | Typ | Max | Unit | Remark |
|-----------------|----------------|-----|-------|------|------|--------|
| Forward Current | I _F | -- | 40 | -- | mA | |
| Forward Voltage | V _F | 22 | 24 | 26.4 | V | |
| Connection mode | - | -- | 4S2P | -- | | |
| LED number | / | | 8 | | pcs | |
| LED Life | | | 20000 | | Hrs | |

Note1: Optical performance should be evaluated at Ta=25℃ only .If LED is driven by high current, high ambient temperature & humidity condition. The life time of LED will be reduced. Operating life means brightness goes down to 50% initial brightness. Typical operating life time is estimated data.

LED CIRCUIT DIAGRAM: 双芯灯



4.3 Block Diagram



5 INTERFACE TIMING

5.1 Data input format for RGB

Table 5-1: 544RGB X 544 Resolution Timing (Fclk: 17, 20, 23; Hsync: 5, 11; Vsync: 5, 11)

| 544RGB X 544 Resolution Timing Table | | | | | | |
|--------------------------------------|----------------|--------|------|------|------|--------|
| Item | Symbol | Min. | Typ. | Max. | Unit | Remark |
| DCLK Frequency | Fclk | 17 | 20 | 23 | MHz | |
| HSYNC | Period Time | Th | 552 | 560 | 592 | DCLK |
| | Display Period | Thdisp | 544 | | | DCLK |
| | Back Porch | Thbp | 4 | 8 | 24 | DCLK |
| | Front Porch | Thfp | 4 | 8 | 24 | DCLK |
| | Pulse Width | Thw | 2 | 4 | 8 | DCLK |
| VSYNC | Period Time | Tv | 560 | 576 | 592 | HSYNC |
| | Display Period | Tvdisp | 544 | | | HSYNC |
| | Back Porch | Tvbp | 8 | 16 | 24 | HSYNC |
| | Front Porch | Tvfp | 8 | 16 | 24 | HSYNC |
| | Pulse Width | Tvw | 2 | 4 | 8 | HSYNC |

Note: 1. The minimum blanking time depends on the GIP timing of the panel specification

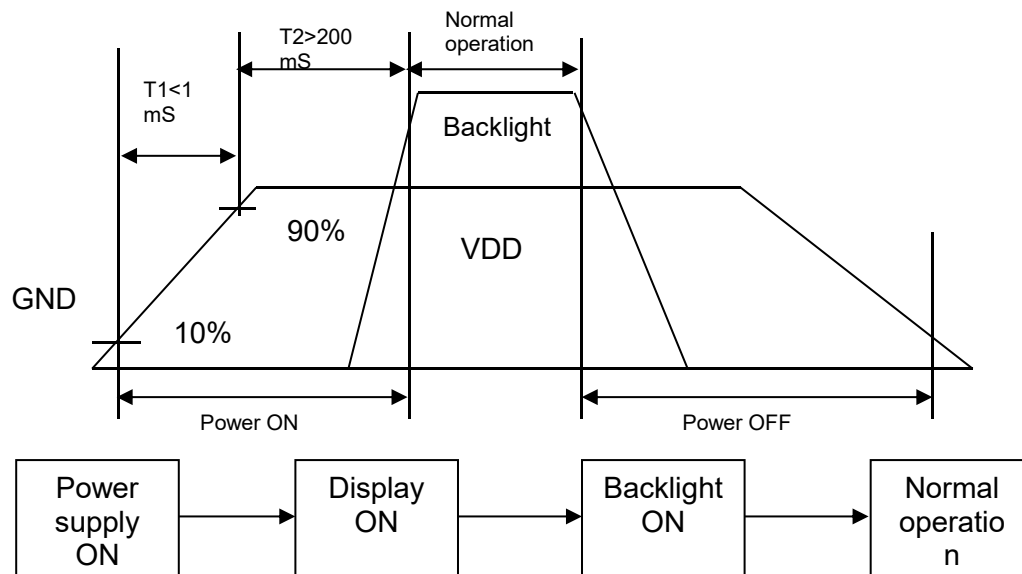
2. To ensure the compatibility of different panels, it is recommended to use the typical setting.

| 480RGB X 480 Resolution Timing Table | | | | | | |
|--------------------------------------|----------------|--------|------|------|------|--------|
| Item | Symbol | Min. | Typ. | Max. | Unit | Remark |
| DCLK Frequency | Fclk | 14 | 16 | 19 | MHz | |
| HSYNC | Period Time | Th | 488 | 960 | 528 | DCLK |
| | Display Period | Thdisp | 480 | | | DCLK |
| | Back Porch | Thbp | 4 | 8 | 24 | DCLK |
| | Front Porch | Thfp | 4 | 8 | 24 | DCLK |
| | Pulse Width | Thw | 2 | 4 | 8 | DCLK |
| VSYNC | Period Time | Tv | 496 | 512 | 528 | HSYNC |
| | Display Period | Tvdisp | 480 | | | HSYNC |
| | Back Porch | Tvbp | 8 | 16 | 24 | HSYNC |
| | Front Porch | Tvfp | 8 | 16 | 24 | HSYNC |
| | Pulse Width | Tvw | 2 | 4 | 8 | HSYNC |

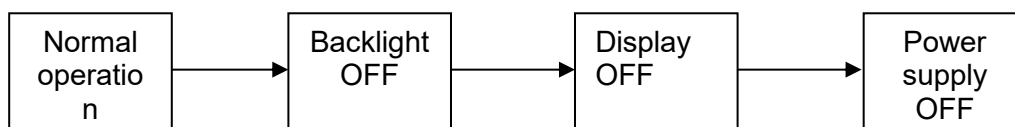
Note: 1. The minimum blanking time depends on the GIP timing of the panel specification

2. To ensure the compatibility of different panels, it is recommended to use the typical setting.

5.2 Power ON/OFF Timing



Power ON sequence



Power OFF sequence

6 Optical Characteristics

$T_a = 25^\circ\text{C}$

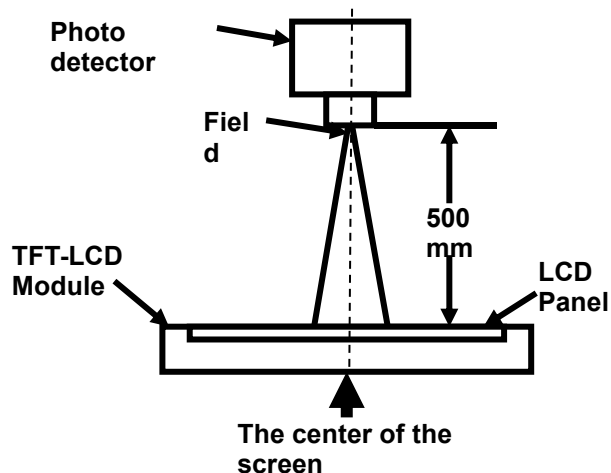
| Item | Symbol | Condition | Min | Typ | Max | Unit | Remark |
|----------------|------------|--------------------|------|------|-----|-----------------|-------------|
| View Angles | θ_T | $CR \geq 10$ | 80 | 85 | - | Degree | Note 2 |
| | θ_B | | 80 | 85 | - | | |
| | θ_L | | 80 | 85 | - | | |
| | θ_R | | 80 | 85 | - | | |
| Contrast Ratio | CR | $\theta = 0^\circ$ | 1000 | 1200 | - | - | Note1 Note3 |
| Response Time | T_{ON} | 25°C | - | 30 | 40 | ms | Note1 Note4 |
| | T_{OFF} | | | | | | |
| Uniformity | U | - | 75 | 80 | - | % | Note1 Note6 |
| NTSC | - | - | 50 | 60 | - | % | Note 5 |
| Luminance | L | | - | 580 | - | cd/m^2 | Note1 Note7 |

Test Conditions:

- $V_F = 24\text{V}$, $I_F = 40\text{mA}$, the ambient temperature is 25°C .
- The test systems refer to Note 1 and Note 2.

Note 1: Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 5 minutes operation, the optical properties are measured at the center point of the LCD screen. All input terminals LCD panel must be ground when measuring the center area of the panel.



| Item | Photo detector | Field |
|----------------|----------------|-------|
| Contrast Ratio | SR-3A | 1° |
| Luminance | | |
| Chromaticity | | |
| Lum Uniformity | | |
| Response Time | BM-7A | 2° |

Note 2: Definition of viewing angle range and measurement system.

viewing angle is measured at the center point of the LCD by CONOSCOPE(ergo-80).

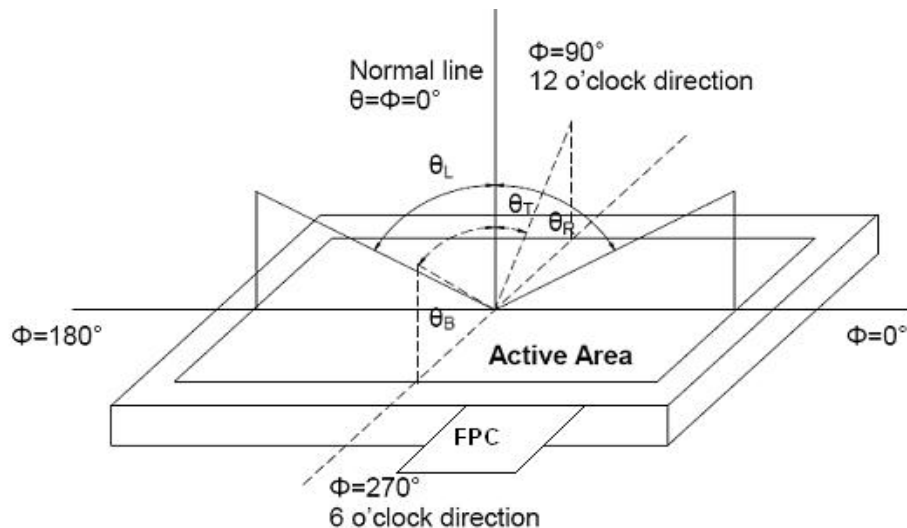


Fig. 1 Definition of viewing angle

Note 3: Definition of contrast ratio

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD is on the "White" state}}{\text{Luminance measured when LCD is on the "Black" state}}$$

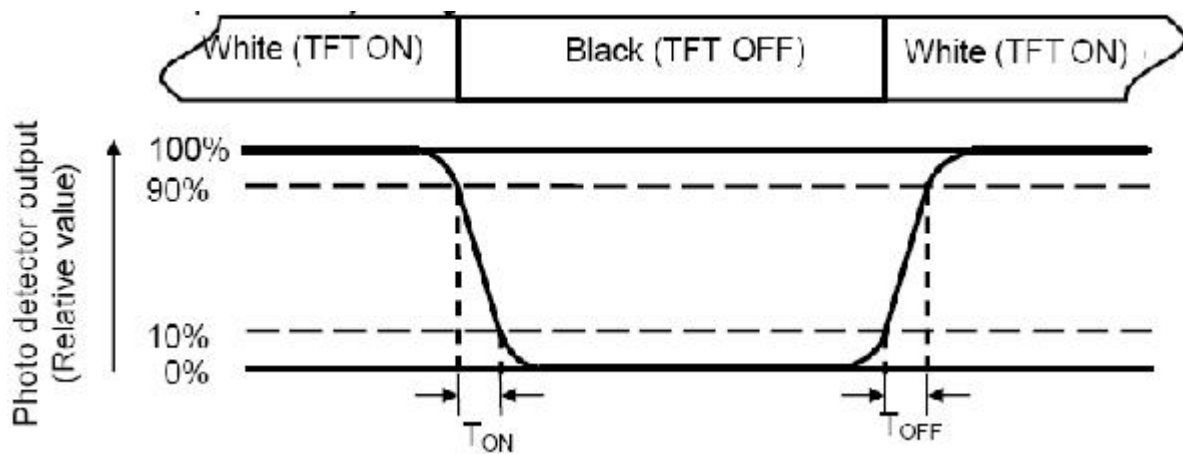
"White state": The state is that the LCD should be driven by V_{white} .

"Black state": The state is that the LCD should be driven by V_{black} .

V_{white} : To be determined V_{black} : To be determined.

Note 4: Definition of Response time

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time (T_{ON}) is the time between photo detector output intensity changed from 90% to 10%. And fall time (T_{OFF}) is the time between photo detector output intensity changed from 10% to 90%.

**Note 5: Definition of color chromaticity (CIE1931)**

Color coordinates measured at center point of LCD.

Note 6: Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (Refer Fig. 2). Every measuring point is placed at the center of each measuring area.

Luminance Uniformity (U) = L_{min} / L_{max}

L-----Active area length W----- Active area width

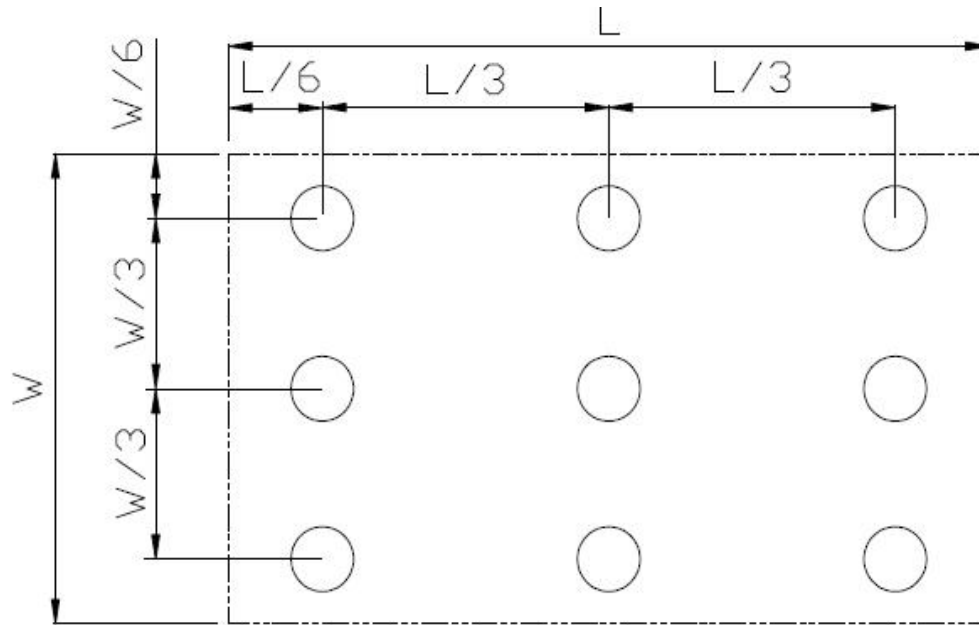


Fig. 2 Definition of uniformity

L_{max} : The measured maximum luminance of all measurement position.

L_{min} : The measured minimum luminance of all measurement position.

Note 7: Definition of Luminance:

Measure the luminance of white state at center point.

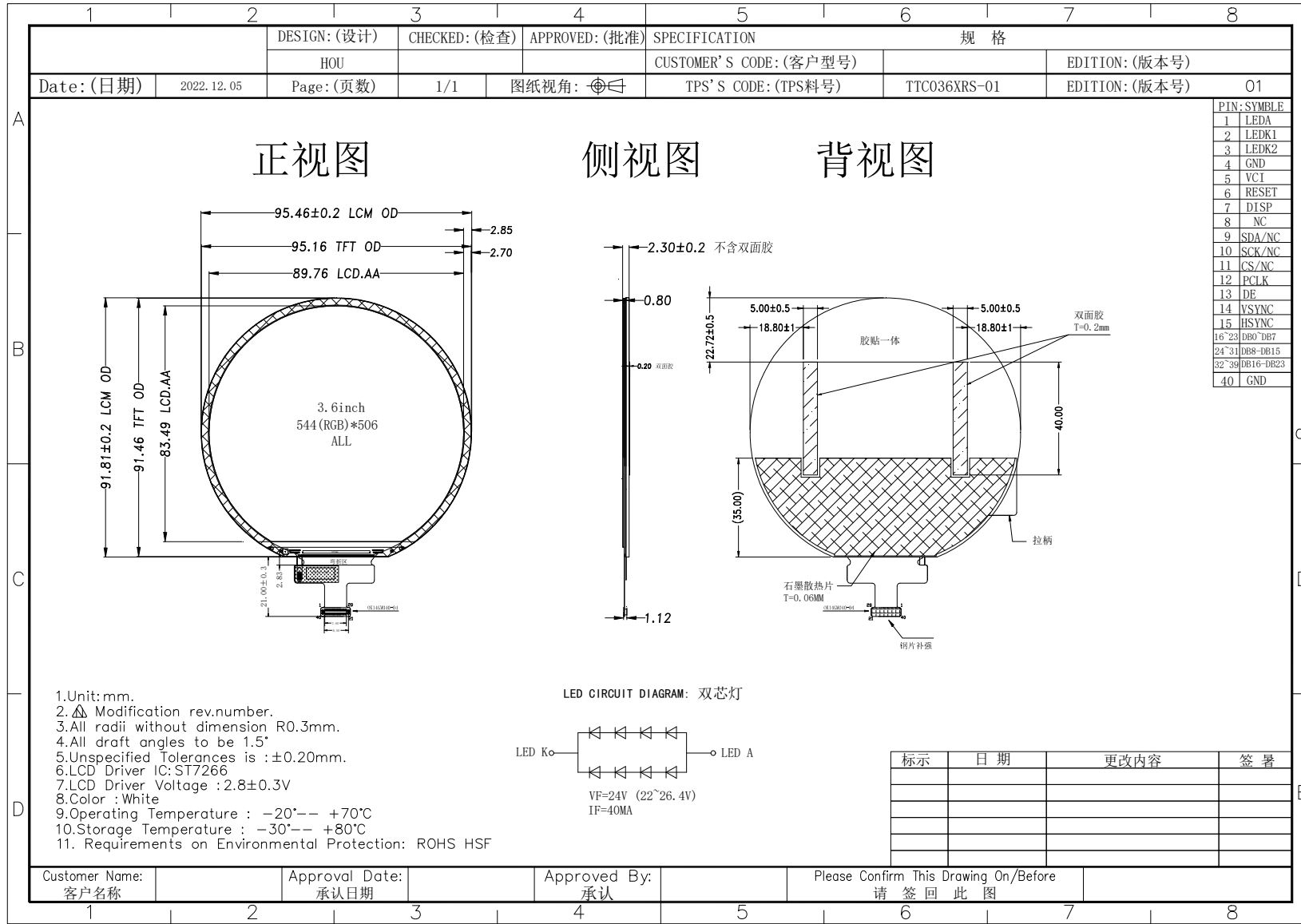
7 Environmental / Reliability Test

| Item | Condition | Time (hrs) | Assessment |
|---------------------------------|--|------------|--|
| High temp. Storage | 80°C | 120 | No abnormalities in functions and appearance |
| High temp. Operating | 70°C | 120 | |
| Low temp. Storage | -30°C | 120 | |
| Low temp. Operating | -20°C | 120 | |
| Humidity | 60°C/ 90%RH | 120 | |
| Thermal Shock(Non-operation) | -0°C ← 25°C →70°C (0.5 hour ← 5 min → 0.5 hour) | 10cycles | |

Remark:

- 1.The test samples should be applied to only one test item.
- 2.Sample size for each test item is 1~10pcs.
- 3.In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judged as a good part.

8 Mechanical Drawing



| PIN | SYMBLE |
|-------|-----------|
| 1 | LEDA |
| 2 | LEDK1 |
| 3 | LEDK2 |
| 4 | GND |
| 5 | VCI |
| 6 | RESET |
| 7 | DISP |
| 8 | NC |
| 9 | SDA/NC |
| 10 | SCK/NC |
| 11 | CS/NC |
| 12 | PCLK |
| 13 | DE |
| 14 | VSYNC |
| 15 | HSYNC |
| 16~23 | DB0~DB7 |
| 24~31 | DB8~DB15 |
| 32~39 | DB16~DB23 |
| 40 | GND |

9 Precautions For Use of LCD Modules

9.1 Handling Precautions

9.1.1.1 The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.

9.1.1.2 If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.

9.1.1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.

9.1.1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.

9.1.1.5 If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:

- Isopropyl alcohol
- Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

- Water
- Ketone
- Aromatic solvents

9.1.1.6 Do not attempt to disassemble the LCD Module.

9.1.1.7 If the logic circuit power is off, do not apply the input signals.

9.1.1.8 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

9.1.1.9 Be sure to ground the body when handling the LCD Modules.

9.1.1.10 Tools required for assembly, such as soldering irons, must be properly ground.

9.1.1.11 To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.

9.1.1.12 The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

9.1.1.13 Storage precautions

9.1.1.14 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.

9.1.1.15 The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:

9.1.1.16 The LCD modules should be stored in the room without acid, alkali and harmful gas.

9.2 Transportation Precautions

The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.