

Manufacturer Certificated





CERT. No.: 282Q19070712006 CERT

CERT. No.: 282E19070712007

Product Specification

Model: TTY270BHT-01

27.0"TFT Display Module(1920*1080)

This module uses RoHS material

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REVISION HISTORY

()Preliminary specification

(●)Final specification

Revision No.	Page	Description of changes	Date	Prepared
P0		Initial Release	2021.11.24	Feng xin
О		Final	2023.9.14	Feng xin



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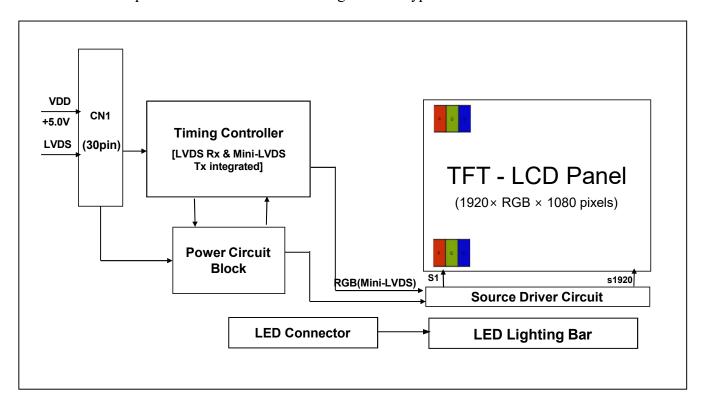
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1.0 GENERAL DESCRIPTION

1.1 Introduction

TTY270BHT-01 is a color active matrix TFT LCD module using amorphous silicon TFT's (Thin Film Transistors) as an active switching devices. This module has a 27 inch diagonall y measured active area with FHD resolutions (1920 horizontal by 1080 vertical pixel array). Each pixel is divided into RED, GREEN, BLUE dots which are arranged in vertical stripe a nd this module can display 16.7M colors. The TFT-LCD panel used for this module is adapt ed for a low reflection and higher color type.



1.2 Features

- LVDS Interface with 2 pixel / clock
- 8-bit (6-bit+FRC) color depth, display 16.7MB colors
- Color Gamut 72%@NTSC(CIE 1931)
- ES8.0 compliant
- High luminance and contrast ratio, low reflection and wide viewing angle
- DE (Data Enable) only
- RoHS/Halogen Free
- Gamma Correction
- Reverse type



1.3 Application

- Desktop Type of PC & Workstation Use
- Slim-Size Display for Stand-alone Monitor
- Display Terminals for Control System
- Monitors for Process Controller

1.4 General Specification

The followings are general specifications at the model TTY270BHT-01.

<Table 1. General Specifications>

Parameter	Specification	Unit	Remarks
Active area	597.888(H) × 336.312(V)	mm	
Number of pixels	1920(H) ×1080 (V)	pixels	
Pixel pitch	0.3114 (H) × 0.3114(V)	mm	
Pixel arrangement	RGB Vertical stripe		
Display colors	16.7M	colors	6-bit+FRC
Display mode	Normally Black		
Dimensional outline	630×368×9.7(w/o PCB);13.5(with PCB)	mm	
Weight	3220typ,3500 max	g	
Power Consumption	24.05W(Typ), 37.03W(MAX)	Watt	
Surface Treatment	Haze 25%, 3H		
Bezel width (L/R/U/D)	14/14/14.5/13.9	mm	
Back-light	ELED		



2.0 ABSOLUTE MAXIMUM RATINGS

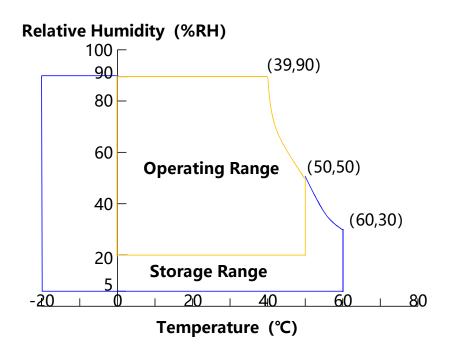
The followings are maximum values which, if exceed, may cause faulty operation or damage to the unit. The operational and non-operational maximum voltage and current values are listed in Table 2.

< Table 2. Absolute Maximum Ratings >

[VSS=GND=0V]

Parameter	Symbol	Min.	Max.	Unit	Remark
Power Supply Voltage	VDD	VSS-0.3	6	V	Ta = 25 ℃
On exeting Temperature	T_OP	0	+50	°C	
Operating Temperature	T_{SUR}	-	+65	°C	
Storage Temperature	T _{ST}	-20	+60	°C	Note 1
Operating Ambient Humidit y	Нор	10	80	%RH	113.5
Storage Humidity	Hst	10	80	%RH	

Note: 1) Temperature and relative humidity range are shown in the figure below. Wet bulb temperature should be 39 °C max. and no condensation of water.





3.0 ELECTRICAL SPECIFICATIONS

3.1 Electrical Specifications

< Table 3. Open Cell Electrical specifications >

 $[Ta = 25 \pm 2 \, ^{\circ}C]$

Parameter		Min.	Тур.	Max.	Unit	Remarks
Power Supply Voltage	VDD	4.5	5	5.5	V	Note1
Power Supply Current	IDD	1	640	1060	mA	Note1
In-Rush Current	IRUSH	1	2.0	3.0	A	Note 2
Permissible Input Ripple Volt age	VRF	-	-	400	mV	Note1,3
High Level Differential Input Threshold Voltage	VIH	+100	-	+300	mV	
Low Level Differential Input Threshold Voltage	VIL	-300	-	-100	mV	
Differential input voltage	VID	200	-	600	mV	
Differential input common m ode voltage	Vem	1.0	1.2	1.5		VIH=100mV, VIL=-100mV
Dayyan Canayantias	PD		3.2	5.3	W	
Power Consumption	PBL		25.52	39.53	W	

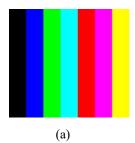
Notes: 1. The supply voltage is measured and specified at the interface connector of LCM.

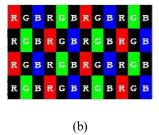
The current draw and power consumption specified is for VDD=5.0V, Frame rate=60Hz

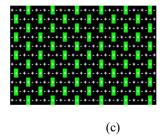
Clock frequency = 74.25 MHz. Test Pattern of power supply current

a) Typ: Color Testb) Max: Skip Subpixel255

c) Flicker Pattern







- 2. Duration of rush current is about 2 ms and rising time of VDD is 520 μ s \pm 20 %
- 3. Ripple Voltage should be covered by Input voltage Spec.
- 4. Calculated value for reference (Input pins*VPIN ×IPIN) excluding inverter loss.



3.2 Backlight Unit

< Table 4. Backlight Unit Electrical Specifications >

Parameter		Min.	Тур.	Max.	Unit	Remarks		
LED Light Bar Input Voltage Per Input Pin	VPI N		50	58	V	Duty 100%		
LED Light Bar Input Current Per Input Pin	IPIN		60	-	mA	Note1,2,		
LED Power Consumption	PBL		21.5	36.5	W	Note 3		
LED Life-Time	-	30,000	-		Hrs	Note 4		

LED bar consists of 119LED packages,7 strings(parallel)*17packages(serial)

Note1: There are one light bar ,and the specified current is input LED chip 100% duty current

Note2: The sense current of each input pin is 60mA, LED Bar (7 Input pins)

Note3: PBL=7 Input pins*VPIN ×IPIN

Note4: The lifetime is determined as the time at which luminance of LED become 50% of the initial brightness or not normal lighting at IPIN=104mA on condition of continuous operating at 25 ± 2 °C

3.3 LED Light Bar

-LED connector: A1009AWV(6)

< Table 5. LED Light Bar>

	Tuole 5. EED Eight Bul-	
Pin No	Symbol	Description
1	IRLED1	LED current sense for string1
2	IRLED2	LED current sense for string2
3	VLED	LED power supply
4	VLED	LED power supply
5	IRLED3	LED current sense for string3
6	IRLED4	LED current sense for string4



4.0 OPTICAL SPECIFICATION

4.1 Overview

The test of Optical specifications shall be measured in a dark room (ambient luminance ≤ 1 lux and temperature = $25\pm2^{\circ}\text{C}$) with the equipment of Luminance meter system (Goniometer system and TOPCONE PR730) and test unit shall be located at an approximate distance 50cm from the LCD surface at a viewing angle of θ and Φ equal to 0° . We refer to $\theta_{\emptyset=0}$ (= θ_3) as the 3 o'clock direction (the "right"), $\theta_{\emptyset=90}$ (= θ_{12}) as the 12 o'clock direction ("upward"), $\theta_{\emptyset=180}$ (= θ_9) as the 9 o'clock direction ("left") and $\theta_{\emptyset=270}$ (= θ_6) as the 6 o'clock direction ("bottom"). While scanning θ and/or \emptyset , the center of the measuring spot on the Display surface shall stay fixed. The measurement shall be executed after 30 minutes warm-up period. VDD shall be 5.0V +/-10% at 25°C. Optimum viewing angle direction is 6 'clock.

4.2 Optical Specifications

[VDD = 5.0V, Frame rate = 60Hz, Clock = 74.25MHz, I_{BL} = 416mA, Ta =25 \pm 2 °C] < Table 5. Module Optical >

		,	1 401 C 5. 111044	1				
Parame	ter	Symbol	Condition	Min.	Тур.	Max.	Unit	Remark
	Horizontal	Θ_3		85	89	ı	Deg.	
Viewing Angle	поптоппан	Θ_9	CR > 10	85	89	-	Deg.	Note 1
range	Mautical	Θ_{12}	CR > 10	Deg.	Note 1			
	Vertical	Θ_6		85	89	-	Deg.	
Luminance Con	trast ratio	CR		700	1000			Note 2
I vaniman as af X	V/1-:4	Y_{w1}		300	350	-		Note 3
Luminance of V	w mile	Y _{w2}		500	550	-		Note 4
Colour gamut		CG		68	72		%	NTSC
W/hite lawin on or	:6:4	ΔΥ1		75	80		%	Note 5
White luminance uniformity		ΔΥ2	$\Theta=0$ °	80	83		%	Note 6
	White	W _x	(Center)		0.313		-	
	Wille	W_{y}	Normal Viewing		0.329]	-	
	Dad	R _x	Angle		0.648		-	
Reproduction	Red	R _y		-0.03	0.341	+0.03	-	Note 7
of color	Cunan	Gx		-0.03	0.323	+0.03	-	Note /
	Green	Gy			0.625		-	
	DI	B _x			0.156		-	
	Blue	B_{y}			0.054		-	
Response Time	GTG	$T_{ m g}$			14	20	ms	Note 8
Cross T	alk	CT		-	-	2.0	%	Note 9



Note:

- 1. Viewing angle is the angle at which the contrast ratio is greater than 10. The viewing are dete rmined for the horizontal or 3, 9 o'clock direction and the vertical or 6, 12 o'clock direction with respect to the optical axis which is normal to the LCD surface.
- 2. Contrast measurements shall be made at viewing angle of θ = 0° and at the center of the LCD surface. Luminance shall be measured with all pixels in the view field set first to white, then t o the dark (black) state. (See FIGURE 1 shown in Appendix) Luminance Contrast Ratio (CR) is defined mathematically.

- 3. Center Luminance of white is defined as the LCD surface. Luminance shall be measured with all pixels in the view field set first to white. This measurement shall be taken at the locations shown in FIGURE 2 for a total of the measurements per display.
- 4. Average (P1~P5) Luminance of white is defined as the LCD surface. Luminance shall be mea sured with all pixels in the view field set first to white. This measurement shall be taken at the locations shown in FIGURE 2 for a total of the measurements per display.
- 5. The White luminance uniformity on LCD surface is then expressed as:
 - $\Delta Y =$ (Minimum Luminance of P1~P13points / Maximum Luminance of P1~P13points) * 100 (See FIGURE 2 shown in Appendix).
- The White luminance uniformity on LCD surface is then expressed as:
 ΔY = (Minimum Luminance of P1~ P5points / Maximum Luminance of P1~ P5points) * 1
 (See FIGURE 2 shown in Appendix).
- 7. The color chromaticity coordinates specified in Table 5. shall be calculated from the spectral data measured with all pixels first in red, green, blue and white. Measurements shall be made at the center of the panel.
- 8. Response time Tg is the average time required for display transition by switching the input signal as below table and is based on Frame rate fV =60Hz to optimize.
 - Each time in below table is defined as appendix Figure 3 and shall be measured by switching the input signal for "any level of gray(bright)" and "any level of gray(dark)"
- 9. Cross-Talk of one area of the LCD surface by another shall be measured by comparing the luminance (Y_A) of a 25mm diameter area, with all display pixels set to a gray level, to the luminance (Y_B) of that same area when any adjacent area is driven dark. (See FIGURE 4 shown in Appendix).



5.0 INTERFACE CONNECTION.

5.1 Electrical Interface Connection

• CN1 Module Side Connector: UJU IS100-L30O-C23or Equivalent

1 RXO0N 2 RXO0P 3 RXO1N 4 RXO1P 5 RXO2N 6 RXO2P 7 BIST 8 RXOCN- 9 RXOCP 10 RXO3N 11 RXO3P	Negative LVDS differential data input Positive LVDS differential data input Negative LVDS differential data input Positive LVDS differential data input Negative LVDS differential data input Positive LVDS differential data input BIST Negative LVDS differential clock input Positive LVDS differential clock input Negative LVDS differential clock input	
3 RXO1N 4 RXO1P 5 RXO2N 6 RXO2P 7 BIST 8 RXOCN- 9 RXOCP 10 RXO3N	Negative LVDS differential data input Positive LVDS differential data input Negative LVDS differential data input Positive LVDS differential data input BIST Negative LVDS differential clock input Positive LVDS differential clock input	
4 RXO1P 5 RXO2N 6 RXO2P 7 BIST 8 RXOCN- 9 RXOCP 10 RXO3N	Positive LVDS differential data input Negative LVDS differential data input Positive LVDS differential data input BIST Negative LVDS differential clock input Positive LVDS differential clock input	
5 RXO2N 6 RXO2P 7 BIST 8 RXOCN- 9 RXOCP 10 RXO3N	Negative LVDS differential data input Positive LVDS differential data input BIST Negative LVDS differential clock input Positive LVDS differential clock input	
6 RXO2P 7 BIST 8 RXOCN- 9 RXOCP 10 RXO3N	Positive LVDS differential data input BIST Negative LVDS differential clock input Positive LVDS differential clock input	
7 BIST 8 RXOCN- 9 RXOCP 10 RXO3N	BIST Negative LVDS differential clock input Positive LVDS differential clock input	
8 RXOCN- 9 RXOCP 10 RXO3N	Negative LVDS differential clock input Positive LVDS differential clock input	
9 RXOCP 10 RXO3N	Positive LVDS differential clock input	
10 RXO3N	•	
	Negative LVDS differential data input	
11 RXO3P		
	Positive LVDS differential data input	
12 RXE0N	Negative LVDS differential data input	
13 RXE0P	Positive LVDS differential data input	
14 GND	Ground	
15 RXE1N	Negative LVDS differential data input	
16 RXE1P	Positive LVDS differential data input	
17 GND	Ground	
18 RXE2N	Negative LVDS differential data input	
19 RXE2P	Positive LVDS differential data input	
20 RXECN	Negative LVDS differential clock input	
21 RXECP	Positive LVDS differential clock input	
22 RXE3N	Negative LVDS differential data input	
23 RXE3P	Positive LVDS differential data input	
24 GND	Ground	
25 SCL	I2C Clock (For VCOM tuning)	
26 SDA	I2C Data (For VCOM tuning)	
27 NC	NC	
28 VIN	Power Supply 5V	
29 VIN	Power Supply 5V	
30 VIN	Power Supply 5V	



6.0 SIGNAL TIMING SPECIFICATION

 $6.1\ The\ DV270FHM\text{-}N11$ is operated by the DE only.

Item	Symbols		Min	Тур	Max	Unit	Note
	Period	tCLK	10.78	13.47	16.16	ns	
DCLK	Frequency	fCLK	61.87	74.25	92.8	MHz	
	Period	tHP	1050	1100	1120	tCLK	
Hsync	Horizontal Valid	tHV	960	960	960	tCLK	
	Horizontal Blank	tHB	90	140	160	tCLK	
	Frequency	fH	56	67.5	84.5	KHz	
	Period	tVP	1110	1125	1251	tHP	
Verma	Vertical Valid	tVV	1080	1080	1080	tHP	
Vsync	Vertical Blank	tVB	30	45	171	tHP	
	Frequency	fV	50	60	75	Hz	
LVDS Receiv er cloc k	Input spread spectr um ratio	SSr	-3%	-	+3%	%	

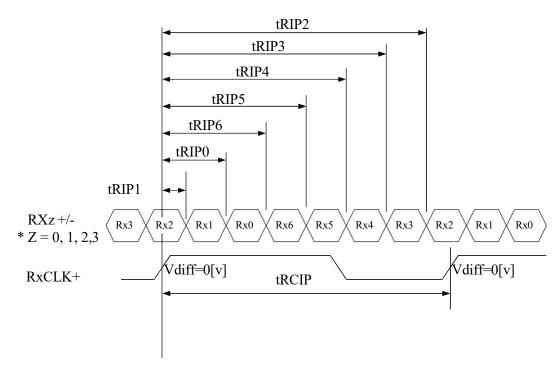


6.2 LVDS Rx Interface Timing Parameter

The specification of the LVDS Rx interface timing parameter is shown in Table 7.

<Table 7. LVDS Rx Interface Timing Specification>

Item	Symbol	Min	Тур	Max	Unit	Remark
CLKIN Period	tRCIP	10.78	13.47	16.16	nsec	
Input Data 0	tRIP1	-0.4	0.0	+0.4	nsec	
Input Data 1	tRIP0	tRCIP/7-0.4	tRCIP/7	tRCIP/7+0.4	nsec	
Input Data 2	tRIP6	$2 \times tRCIP/7-0.4$	2 ×tRCIP/7	$2 \times \text{tRCIP/7+0.4}$	nsec	
Input Data 3	tRIP5	$3 \times tRCIP/7-0.4$	3 × tRCIP/7	$3 \times \text{tRCIP/7+0.4}$	nsec	
Input Data 4	tRIP4	$4 \times tRCIP/7-0.4$	4 × tRCIP/7	$4 \times tRCIP/7+0.4$	nsec	
Input Data 5	tRIP3	$5 \times \text{tRCIP/7-0.4}$	5 × tRCIP/7	$5 \times \text{tRCIP/7+0.4}$	nsec	
Input Data 6	tRIP2	6 × tRCIP/7-0.4	6 × tRCIP/7	6 × tRCIP/7+0.4	nsec	

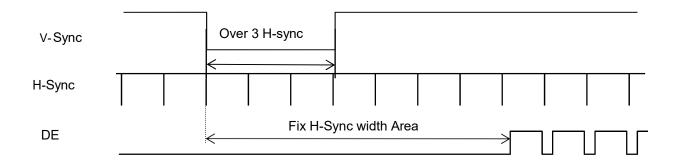


^{*} Vdiff = (RXz+)-(RXz-),...,(RXCLK+)-(RXCLK-)



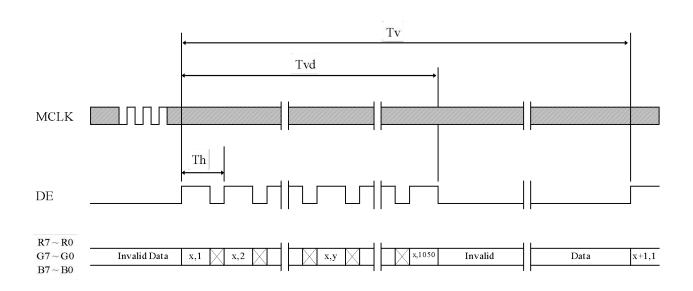
7.0 SIGNAL TIMING WAVEFORMS OF INTERFACE SIGNAL

7.1 Sync Timing Waveforms



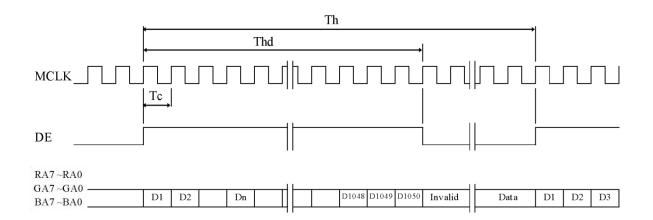
- 1) Need over 3 H-sync during V-Sync Low
- 2) Fix H-Sync width from V-Sync falling edge to first rising edge

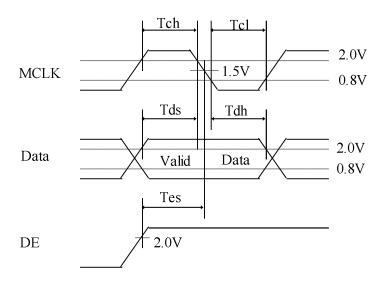
7.2 Vertical Timing Waveforms





7.3 Horizontal Timing Waveforms







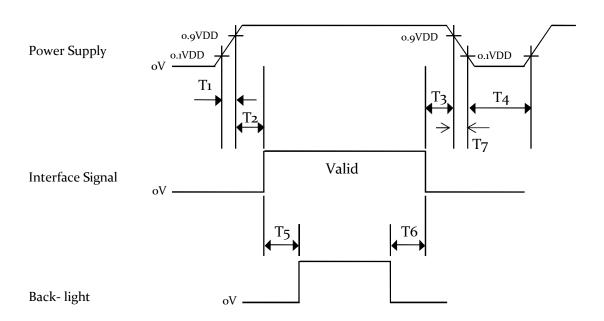
8.0 INPUT SIGNALS, BASIC DISPLAY COLORS & GRAY SCALE OF COLORS

Color & Gray Scale			RED DATA							GREEN DATA								BLUE DATA							
Color & G	ray Scale	R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	Gl	G0	В7	В6	B5	B4	В3	B2	В1	B0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Basic Colors	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Darker		0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale	0				,	<u> </u>								1								1			
of RED	0	1								,	ļ							,	ļ						
	Brighter	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
Gray Scale	Darker	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
of GREEN	0	<u> </u>							<u> </u>											1					
of GREEN	0				,	\downarrow				↓							<u></u>								
	Brighter	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0
	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
Gray Scale	Darker	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
of BLUE	0				,	<u> </u>							ĺ	1				<u> </u>							
OI BLUE	0					\downarrow							. ,	ļ								ļ			
	Brighter	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
[0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1
Gray Scale	Darker	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0
						<u> </u>								1								1			
of WHITE	0					\downarrow								Į								Į			
	Brighter	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1
	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1



9.0 POWER SEQUENCE

To prevent a latch-up or DC operation of the LCD module, the power on/off sequence s hall be as shown in below



- \bullet 0.5 ms \leq T1 \leq 10 ms
- \bullet 0 \leq T2 \leq 50 ms
- \bullet 0 \leq T3 \leq 50 ms
- \bullet 1 sec \leq T4
- \bullet 500 ms \leq T5
- \bullet 200 ms \leq T6

Notes:

- 1. When the power supply VDD is 0V, keep the level of input signals on the low or keep high impedance.
- 2. Do not keep the interface signal high impedance when power is on.
- 3. Back Light must be turn on after power for logic and interface signal are valid.
- 4. T7 decreases smoothly, there is none re-bouncing voltage.



10.0 MECHANICAL CHARACTERISTICS

10.1 Dimensional Requirements

FIGURE 6 (located in Appendix) shows mechanical outlines for the model DV270FHM-N11. Other parameters are shown in Table 8.

< Table 8. Dimensional Parameters>

Parameter	Specification	
Dimensional outline	630×368×9.5 (w/o PCB);13.5(with PCB)	mm
Weight	3220typ,3500 max	gram
Active area	597.888(H) × 336.312(V)	mm
Pixel pitch	$0.3114(\mathrm{H}) \times 0.3114(\mathrm{V})$	mm
Number of pixels	$1920(H) \times 1080 (V) (1 \text{ pixel} = R + G + B \text{ dots})$	pixels

10.2 Anti-Glare and Polarizer Hardness.

The surface of the LCD has an anti-glare coating to minimize reflection and a coating to reduce scratching.

10.3 Light Leakage

There shall not be visible light from the back-lighting system around the edges of the screen as seen from a distance 50cm from the screen with an overhead light level of 350lux.



11.0 RELIABLITY TEST

The Reliability test items and its conditions are shown in below. <Table 9 Reliability Test Parameters >

No	Test Items	Conditions		
1	High temperature & high humid ity storage test	Ta = 60 °C, 90%RH, 240hrs		
2	Low temperature storage test	Ta = -20 °C, 240 hrs		
3	High temperature & high humid ity operation test	Ta = 50 °C, 80%RH, 240hrs		
4	High temperature operation test	Ta = 60 °C, 240hrs		
5	Low temperature operation test	Ta = -5°C, 240hrs		
6	Thermal shock	$Ta = -20 ^{\circ}\text{C} \leftrightarrow 60 ^{\circ}\text{C} (0.5 \text{ hr}), 100 \text{ cycle}$		
7	Vibration test (non-operating)	Frequency $1 \sim 200 \text{ Hz}$, Gravity / AMP 1.2 Grms Period $+X, -Y, 30 \text{ min}, \pm Z 1 \text{Hr}$		
8	Electro-static discharge test	Air : 150 pF, 330Ω, \pm 15 KV Contact : 150 pF, 330Ω, \pm 8 KV		



12.0 PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD Module.

12.1 Mounting Precautions

- Use finger-stalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.
- You must mount a module using specified mounting holes (Details refer to the drawings)
- You should consider the mounting structure so that uneven force (ex. Twisted stress, Concentrated stress)is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- Do not apply mechanical stress or static pressure on module; Abnormal display cause by pressing some parts of module during assembly process, do not belong to product failure, the press should be agreed by two sides.
- Determine the optimum mounting angle, refer to the viewing angle range in the specification for each model.
- Do not apply mechanical stress or static pressure on module , and avoid impact, vibration and falling.
- Acetic acid type and chlorine type materials for the cover case are not desirable because
 the former generates corrosive gas of attacking the polarizer at high temperature and the
 latter causes circuit break by electro-chemical reaction.
- Protection film for polarizer on the module should be slowly peeled off before display.
- Be careful to prevent water & chemicals contact the module surface.
- You should adopt radiation structure to satisfy the temperature specification.
- Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment.
 Do not touch the surface of polarizer for bare hand or greasy cloth. (Some cosmetics are detrimental to the polarizer.)
- When the surface becomes dusty, please wipe gently with absorbent cotton or other soft
 materials like chamois soaks with petroleum benzine. Normal-hexane & alcohol is
 recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use
 acetone, toluene, because they cause chemical damage to the polarizer.
- Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading..

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- This module has its circuitry PCB's on the rear side and Driver IC, should be handled carefully in order not to be stressed.
- Avoid impose stress on PCB and Driver IC during assembly process, Do not drawing, bending, COF package & wire
- Do not disassemble the module.

12.2 Operating Precautions

- Do not connector or disconnect the cable to/from the Module at the "Power On" Condition.
- When the module is operating, do not lose CLK, ENAB signals. If any one of these signals i s lost, the module would be damaged.
- Obey the supply voltage sequence. If wrong sequence is applied, the module would be dam aged.
- Do not allow to adjust the adjustable resistance or switch
- The electrochemical reaction caused by DC voltage will lead to LCD module degradation, s
 o DC drive should be avoided.
- The LCD modules use C-MOS LSI drivers, so customers are recommended that any unuse d input terminal would be connected to Vdd or Vss, do not input any signals before power i s turn on, and ground you body, work/assembly area, assembly equipment to protect again st static electricity.
- Do not exceed the absolute maximum rating value. (supply voltage variation, input voltage variation, variation in part contents and environmental temperature, and so on) Otherwise the Module may be damaged.
- Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.
- Design the length of cable to connect between the connector for back-light and the converte
 r as shorter as possible and the shorter cable shall be connected directly, The long cable b
 etween back-light and Converter may cause the Luminance of LED to lower and need a hig
 her startup voltage
- The cables should be as short as possible between System Board and PCB interface.
- Connectors are precision devices to transmit electrical signals, and operators should plug in parallel
- Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.



12.3 Electrostatic Discharge Precautions

- Avoid the use work clothing made of synthetic fibers. We recommend cotton clothing or other conductivity-treated fibers.
- Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc.
- Do not close to static electricity to avoid product damage.
- Do not touch interface pin directly.

12.4 Precautions for Strong Light Exposure

• Do not leave the module operation or storage in Strong light . Strong light exposure causes degradation of polarizer and color filter.

12.5 Precautions for Storage

A. Atmosphere Requirement

11. Ittinosphere requirement							
ITEM	UNIT	MIN	MAX				
Storage Temperature	(°C)	5	40				
Storage Humidity	(%rH)	40	75				
Storage Life	6 months						
Storage Condition	 The storage room should be equipped with a dark and good ventilation facility. Prevent products from being exposed to the direct sunlight, moisture and water. The product need to keep away from organic solvent and corrosive gas. Be careful for condensation at sudden temperature change. Storage condition is guaranteed under packing conditions. 						

B. Package Requirement

- The product should be placed in a sealed polythene bag.
- Product Should be placed on the pallet, Which is away from the floor, Be cautions not to pile the product up.
- The polarizer surface should not come in contact with any other object. It is recommended that they be stored in the container in which they were shipped.
- As the original protective film, do not use the adhesive protective film to avoid change of Pol color and characteristic.

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12.6 Precautions for protection film

- Remove the protective film slowly, keeping the removing direction approximate 30-degree not vertic al from panel surface, If possible, under ESD control device like ion blower, and the humidity of wor king room should be kept over 50%RH to reduce the risk of static charge.
- People who peeled off the protection film should wear anti-static strap and grounded well.

12.7 Appropriate Condition for Commercial Display

-Generally large-sized LCD modules are designed for consumer applications. Accordingly, long-term display like in Commercial Display application, can cause uneven display including image sticking. To optimize module's lifetime and function, several operating usages are required.

- 1. Normal operating condition
- Temperature: 20±15°C
- Operating Ambient Humidity: 55±20%
- Display pattern: dynamic pattern (Real display)
- Well-ventilated place is recommended to set up Commercial Display system
- 2. Special operating condition
 - a. Ambient condition
 - Well-ventilated place is recommended to set up Commercial Display system.
 - b. Power and screen save
 - Periodical power-off or screen save is needed after long-term display.
 - c. As the low temperature, the response time is greatly delayed. As the high temperatures (higher than t he operating temperature) the LCD module may turn black screen. The above phenomenon cannot expl ain the failure of the display. When the temperature returns to the normal operating temperature, the LCD module will return to normal display.
 - d. When expose to drastic fluctuation of temperature (hot to cold or cold to hot) ,the LCD module may be affected; Specifically, drastic temperature fluctuation from cold to hot ,produces dew on the LCD module 's surface which may affect the operation of the polarizer and LCD module
 - e. Do not exceed the absolute maximum rating value. (supply voltage variation, input voltage variation, variation in part contents and environmental temperature, and so on) Otherwise the Module may be damaged.
 - f. Products exposed to low temperature environment for a long time, need to carry out necessary protection, low temperature environment is usually refrigerators, vending machine Etc...
 - g. Long time and large angle forward use or unconventional use , It is strongly recommended to contact TPSfor filed application engineering advice

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h. Product reliability and functions are only guaranteed when the product is used under right operation usages. If product will be used in extreme conditions such as high temperature, high humidity, high altitude, special display images, running time, long time operation, outdoor operation, etc. It is strongly recommended to contact TPS for filed application engineering advice. Otherwise, its reliability and function may not be guaranteed. Extreme conditions are commonly found at airports, transit stations, banks, stock market and controlling systems.

- 3. Operating usages to protect against image sticking due to long-term static display.
 - a. Suitable operating time: under 20 hours a day.
 - b. Static information display recommended to use with moving image.
 - Cycling display between 5 minutes' information(static) display and 10 seconds' moving image.
 - c. Background and character (image) color change
 - Use different colors for background and character, respectively.
 - Change colors themselves periodically.
 - d. Avoid combination of background and character with large different luminance.
 - 1) Abnormal condition just means conditions except normal condition.
 - 2) Black image or moving image is strongly recommended as a screen save
- 4. Lifetime in this spec. is guaranteed only when Commercial Display is used according to operating usages.
- 5. Module should be turned clockwise based on front view when used in portrait mode.

12.8 Other Precautions

A. LC Leak

- If the liquid crystal material leaks from the panel, it is recommended to wash the LC with aceton e or ethanol and then burn it.
- If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, skin or clothes, it has to be washed away thoroughly with soap.
- If LC in mouth, mouth need to be washed, drink plenty of water to induce vomiting and follow medical advice.
- If LC touch eyes, eyes need to be washed with running water at least 15 minutes.

B. Rework

• When returning the module for repair or etc., Please pack the module not to be broken. We recommend to use the original shipping packages.



13.0 PRODUCT SERIAL NUMBER

-TBD



14.0 Packing 14.1 Packing Order -TBD



14.2 Packing Note

-TBD

14.3 Box label

-TBD



15.0 APPENDIX

Figure 1. Measurement Set Up

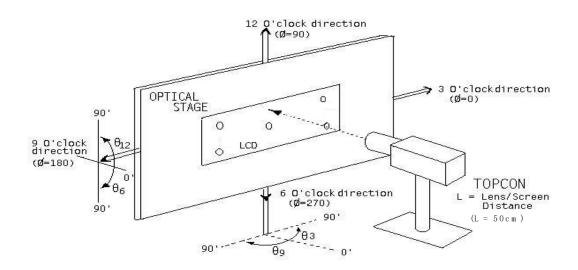


Figure 2. White Luminance and Uniformity Measurement Locations (13 points)

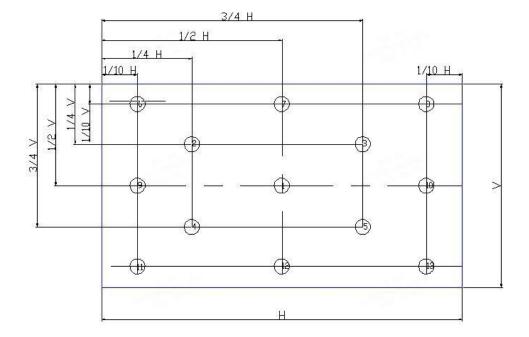
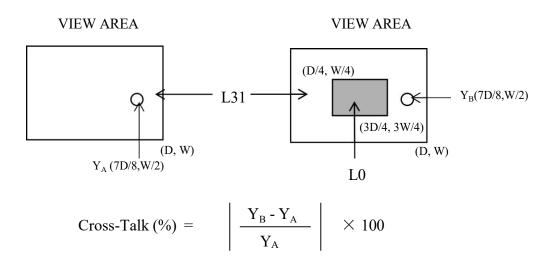




Figure 3. Response Time Testing

Figure 4. Cross Modulation Test Description



Where: $Y_A = Initial luminance of measured area (cd/m²)$

 Y_B = Subsequent luminance of measured area (cd/m²)

The location measured will be exactly the same in both patterns

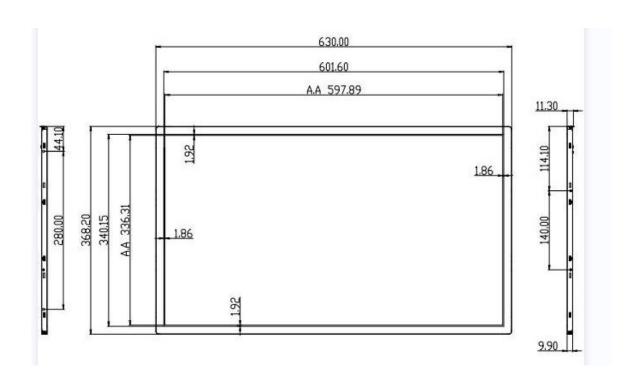




Figure 5. TFT-LCD Module Outline Dimensions (Rear view, Horizontal placement)

