

Manufacturer Certificated



CERT. No.: 282Q19070712006

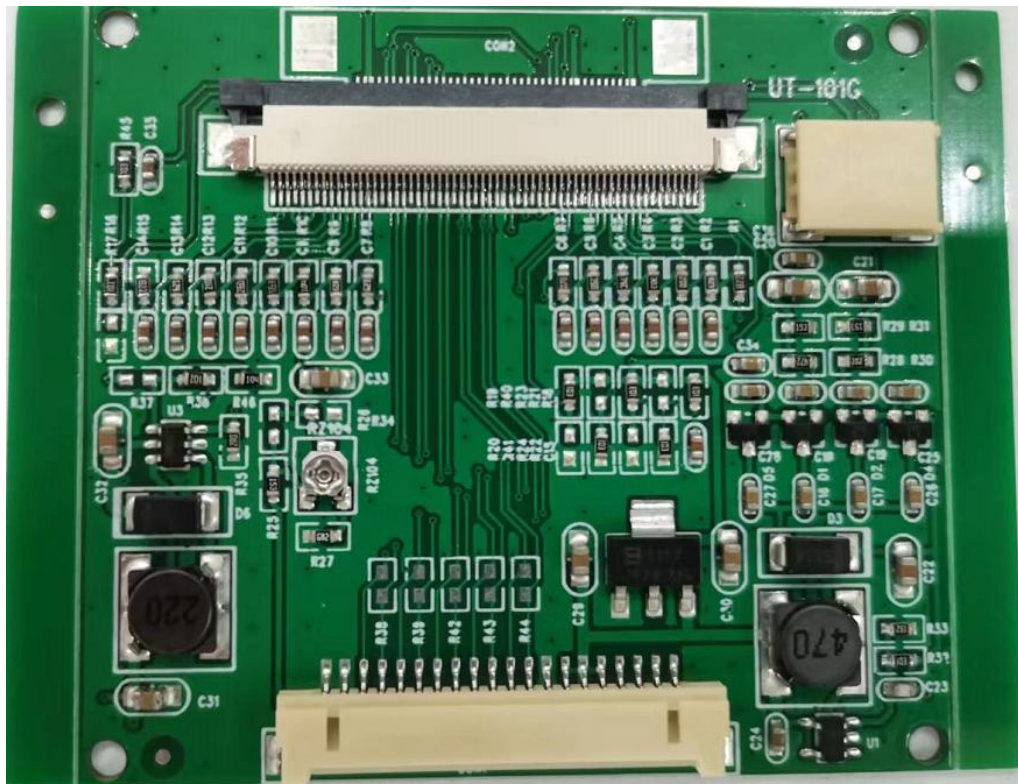
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Product Specification

Model: TBJ101LV-01

LVDS Display Board

This module uses ROHS material



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1. Record of Revision

Version	Revise Date	Content	Remark
V1.0	2017/06/25	-	-

2. General Specifications

Feature		Spec
Characteristics	Size	10.1inch
	LCM Resolution supported	1024(horizontal)*600(Vertical)
	Interface	LVDS
	Connect type	Connector
	PCBA size	60.0*54.0mm
	LCM connector	60PIN/0. 5S
	LVDS connector (input)	20PIN/1. 0S (DF14)

3.Electrical Characteristics

Item	Symbol	MIN	Typ	MAX	Unit
Supply Voltage	VDD	-	5.0	-	V
Direct Current	Id	-	250	-	mA
Operating Temperature	TOPR	-20	-	70	°C
Storage Temperature	TSTG	-30	-	80	°C

4.PIN-MAP

4.1 PIN-MAP-LVDS(CON1)

Pin No.	Symbol	Function
1	5V	Backlight on/off
2	NC	NC
3	NC	NC
4	NC	NC
5	VCC	Power supply (+5.0V)
6	VCC	Power supply (+5.0V)
7	VCC	Power supply (+5.0V)
8	GND	GND
9	RXCLK+	DCLK+
10	RXCLK-	DCLK—
11	RXIN2+	Channel 2+
12	RXIN2-	Channel 2-
13	RXIN1+	Channel 1+
14	RXIN1-	Channel 1-
15	RXIN0+	Channel 0+
16	RXIN0-	Channel 0—
17	RXIN3+	Channel 3+
18	RXIN3-	Channel 3—
19	GND	GND
20	GND	GND

4.2 PIN-MAP-LCM(CON3)

Pin No.	Symbol	Function
1	AGND	Analog ground
2	AVDD	Analog ground
3	DVDD	Digital power
4	GND	Digital ground
5	VCOM	Not connect
6	DVDD	Digital power
7	GND	Digital ground
8	V14	No connect
9	V13	No connect
10	V12	No connect
11	V11	No connect

12	V10	No connect
13	V9	No connect
14	V8	No connect
15	GND	Digital ground
16	DVDD_LVDS	LVDS power
17	GND	Digital ground
18	PIND3	Positive LVDS differential data input
19	NIND3	Negative LVDS differential data input
20	GND	Digital ground
21	PINC	Positive LVDS differential clock input
22	NINC	Negative LVDS differential clock input
23	GND	Digital ground
24	PIND2	Positive LVDS differential data input
25	NIND2	Negative LVDS differential data input
26	GND	Digital ground
27	PIND1	Positive LVDS differential data input
28	NIND1	Negative LVDS differential data input
29	GND	Digital ground
30	PIND0	Positive LVDS differential data input
31	NIND0	Negative LVDS differential data input
32	GND	Digital ground
33	GND_LVDS	LVDS ground
34	GRB	Global reset pin.Active low to enter reset state Suggest to connecting with an RC reset circuit for stability Normally pull high.(R=100K Ω ,C=0.1uF)
35	STBYB	Standby mode,normally pull high STBYB=1,normal operation STBYB=0,timing control,source driver will turn off,all output are higt-Z
36	SHLR	Left or right display control
37	DVDD	Digital power
38	UPDN	Analog ground
39	AGND	Analog ground
40	AVDD	Analog ground
41	VCOM	Not connect
42	DITH	Dithering function enable control.Normally pull low DITHER="1",Enable internal dithering function DITHER="0",Disable internal dithering function
43	GND	Digital ground

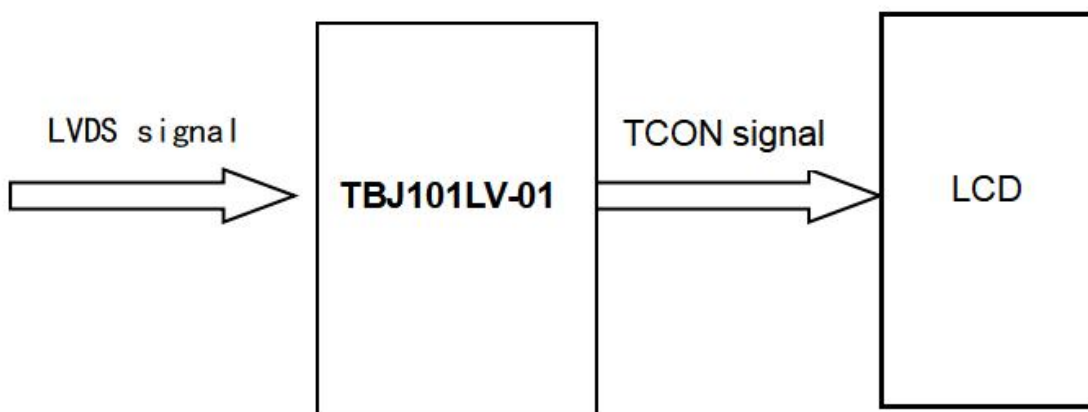
44	DVDD	Digital Power
45	GND	Digital ground
46	V7	Not connect
47	V6	Not connect
48	V5	Not connect
49	V4	Not connect
50	V3	Not connect
51	V2	Not connect
52	V1	Not connect
53	GND	Digital ground
54	DVDD	Digital Power
55	SELB	6bit/8bit mode select SELB="0", LVDS input data is 8bits SELB="1", LVDS input data is 6bits
56	VGH	Positive power for TFT
57	DVDD	Digital power for Gate IC
58	VGL	Negative power for TF
59	GND	Digital ground for Gate IC
60	NC	No connect

4.2 PIN-MAP-LCM(CON2 option)

Pin No.	Symbol	Function
1	VCOM	Common Voltage
2	VDD	2.5V
3	VDD	2.5V
4	NC	No connection
5	Reset	Global reset pin
6	STBYB	Standby mode, Normally pulled high STBYB = "1", normal operation STBYB = "0", timing controller, source driver will turn off, all output are High-Z
7	GND	Ground
8	RXIN0-	- LVDS differential data input
9	RXIN0+	+ LVDS differential data input
10	GND	Ground
11	RXIN1-	- LVDS differential data input
12	RXIN1+	+ LVDS differential data input
13	GND	Ground
14	RXIN2-	- LVDS differential data input
15	RXIN2+	+ LVDS differential data input

16	GND	Ground
17	RXCLKIN-	- LVDS differential clock input
18	RXCLKIN+	+ LVDS differential clock input
19	GND	Ground
20	RXIN3-	- LVDS differential data input
21	RXIN3+	+ LVDS differential data input
22	GND	Ground
23	NC	No connection
24	NC	No connection
25	GND	Ground
26	NC	No connection
27	DIMO	Backlight CABC controller signal output
28	SELB	6bit/8bit mode select
29	AVDD	8.3V~8.4V
30	GND	Ground
31	LED-	LED Cathode
32	LED-	LED Cathode
33	L/R	Horizontal inversion
34	U/D	Vertical inversion
35	VGL	-6.8V~-7V
36	CABCEN1	CABC H/W enable
37	CABCEN0	CABC H/W enable
38	VGH	21.9V-22V
39	LED+	LED Anode
40	LED+	LED Anode

5. Block Diagram



6. Mechanical Drawing

